

ABSTRACT OF THE DISCLOSURE

A translation circuit for mediating between a fiber-optic controller chip and a host device. The translation circuit may be on a fiber-optic transponder. The controller chip includes a phase locked loop that outputs a short synchronization signal when a hunting frequency passes through a target data signal frequency while hunting for a data signal and outputs a synchronization signal when the phase locked loop is locked onto a data signal. The translation circuit distinguishes between the synchronization signals and generates a lock signal when the phase locked loop is locked onto a data signal, but does not when the hunting frequency passes through the target data signal frequency. The lock signal may be used by a host device into which the fiber-optic transponder has been installed. Errors from misinterpreted signals can thus be mitigated.

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